

**REMARKS/ARGUMENTS**

1. In the above referenced Office Action, the Examiner rejected claims 1-3, and 6 under 35 USC § 102 (b) as being anticipated by Hattangadi (U.S. Patent No. 5,087,835). The Examiner objected to claims 4, 5, 7, and 8 have been objected to for being dependent upon a rejected base claim but would be allowable if rewritten in independent form including all of the limitations of the base claim and all intervening claims. The Examiner has allowed claims 9 - 18. These rejections have been traversed and, as such, the applicant respectfully requests reconsideration of the allowability of claims 1 - 8.

2. Claims 1-3, and 6 have been rejected under 35 USC § 102 (b) as being anticipated by Hattangadi. The applicant respectfully disagrees.

Hattangadi, as shown in Figure 1, includes two clock inputs PHI1 and PHI2 (Column 2, line 45 - Column 3, line 29). As is presently claimed, the edge detection is done independently of a clock signal. As such, Hattangadi, which includes two clock inputs, cannot anticipate an edge detection circuit that operates independently of a clock signal.

3. The applicant appreciates the allowance of claims 9 - 18, but disagrees with one of the points of novelty being the specific configuration of the filter module. The applicant believes that any filter that performs in accordance with the functional aspects of the claims would fall under the present claims.

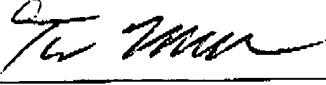


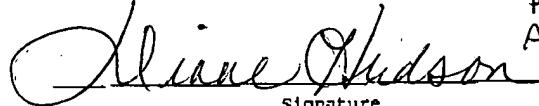
For the foregoing reasons, the applicant believes that claims 1 - 8 are in condition for allowance and respectfully request that they be passed to allowance.

The Examiner is invited to contact the undersigned by telephone or facsimile if the Examiner believes that such a communication would advance the prosecution of the present invention.

RESPECTFULLY SUBMITTED,

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